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APPLICANTS

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** CONTINUING DATA ***** None *TS*** FOREIGN APPLICATIONS ***** None *TS*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>[Signature]</i> Examiner's Signature Initials	STATE OR COUNTRY OR	SHEETS DRAWING 4	TOTAL CLAIMS 27	INDEPENDENT CLAIMS 6
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Best Available Copy

TITLE

Multistage clock delay circuit and method

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